# High Frequency, Low $I_{Q}$ Synchronous Step-Down Controller 

## DESCRIPTIOn

Demonstration circuit DC2834A is a DC/DC synchronous step-down converter featuring the LTC ${ }^{\circledR} 7803$ (MSE package), a spread spectrum or constant frequency current mode synchronous step-down controller. The DC2834A generates a 3.3 V of output voltage.

The 500 kHz constant switching frequency operation results in a small and efficient circuit.

The main features of this board include:

- Wide input voltage range: from 5 V to 38 V
- High load current, up to 20A
- Extremely low quiescent current: $15 \mu \mathrm{~A}$ in sleep mode and as low as $1 \mu \mathrm{~A}$ at shutdown
- Ability to select spread spectrum or fixed frequency
- Selectable pulse-skipping, forced continuous operation or low ripple Burst Mode ${ }^{\circledR}$ operation at light loads
- Synchronization with external clock
- The DC2834A supports Rense or inductor DCR current sensing (optional).
The converter provides high output voltage accuracy (typically $\pm 2 \%$ ) over wide load range with no minimum load requirement.
The DC2834A supports two ways of biasing the controller: directly from the input voltage or output rail through EXTV ${ }_{\text {CC }}$. The third possibility is connecting an external voltage source to EXTV ${ }_{C C}$ terminal.

The DC2834A supports extremely wide switching frequency range from 100 kHz to 3 MHz . The spread spectrum operation reduces the peak radiated and conducted noise to simplify compliance with electromagnetic interference (EMI) standards.

The DC2834A has a small circuit footprint, is a high performance and high density solution for telecom, automotive and Power over Ethernet applications.

Design files for this circuit board are available.
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## BOARD PHOTO



Specifications are at $\mathrm{T}_{\mathrm{A}}=25^{\circ} \mathrm{C}$

| PARAMETER | CONDITIONS | MIN | UNITS |
| :---: | :---: | :---: | :---: |
| Minimum Input Supply Voltage |  | 5 | V |
| Maximum Input Supply Voltage |  | 38 | V |
| Output Voltage Range | $\mathrm{V}_{\text {IN }}=5 \mathrm{~V}$ to $35 \mathrm{~V}, \mathrm{I}_{\text {OUT } 1}=0 \mathrm{~A}$ to 20 A | $3.3 \pm 2 \%$ | V |
| Typical Switching Frequency |  | 500 | kHz |
| Typical Output Ripple (VOUT, 3.3V) | $\mathrm{I}_{\text {LOAD }}=10 \mathrm{~A}$ | 50 | mV |
| Efficiency Typical (V ${ }_{\text {OUT }}$, 3.3V, $\mathrm{V}_{\text {IN }} 12 \mathrm{~V}$ ) | $\mathrm{I}_{\text {LOAD }}=10 \mathrm{~A}$ | 94.5 | \% |
| Maximum Output Current |  | 20 | A |

## PUICK START PROCEDURE

Demonstration circuit 2834 is easy to set up to evaluate the performance of the LTC7803 controllers. Refer to Figure 1 for proper measurement equipment setup and follow the procedure below:
NOTE: When measuring the input or output voltage ripple, care must be taken to avoid a long ground lead on the oscilloscope probe. Measure the input or output voltage ripple by touching the probe tip directly across the $\mathrm{V}_{\text {IN }}$ or $V_{\text {OUt }}$ and GND terminals. See Figure 2 for proper scope probe technique.

1. Place jumper RUN (J2) in ON position, place jumper MODE (JP1) in PULSE SKIP position, place jumper FREQ SET (JP3) into FIX FREQ position.
2. With power off, connect the input power supply to $V_{I N}$ and GND.

Turn the input power source on and slowly increase the input voltage to 12 V . Be careful not to exceed 38 V .
NOTE: Make sure that the input voltage $\mathrm{V}_{\mathrm{IN}}$ does not exceed 38V. If higher operating voltage is required, power components with higher voltage ratings should be used.
3. Check for the proper output voltage of 3.3 V . If there is no output, temporarily disconnect the load to make sure that the load is not set too high.
4. Once the proper output voltages are established, adjust the load within the operating range and observe the output voltage regulation, ripple voltage, efficiency and other parameters.
To synchronize DC2834A with external clock insert jumper FREQ SET (JP3) in SYNC position and apply clock signals to terminal SYNC (E4).

## CONVERTER EFFICIENCY

DC2834SA approaches 95\% efficiency at 12V input voltage generating 3.3 V at 20 A , see Figure 3 . The converter efficiency varies for given load current at different input voltages, which is illustrated by Figure 3 as well. The thermal image of DC2834 at full load presented Figure 4. All efficiency measurements were conducted at room temperature, natural convection cooling with no air flow.

## DC2834A SPREAD SPECTRUM

The demo board DC2834A shipped with fixed frequency operation setting. To employ the spread spectrum operation, insert the jumper FREQ SET (JP3) in into SPREAD position. In this setting the switching frequency will change in $\pm 15 \%$ range relatively to the preset value.

## PUICK START PROCEDURE



Figure 1. Proper Measurement Equipment Setup


Figure 2. Measuring Input or Output Ripple

## DEMO MANUAL DC2834A

## PUICK START PROCEDURE



Figure 3. DC2834A, Efficiency vs Load for Different Input Voltages, Burst Mode Operation


Figure 4. Thermal Image, $\mathrm{V}_{\text {IN }} \mathbf{1 2 V}, \mathrm{V}_{\text {OUT }} 3.3 \mathrm{~V}$ at $20 \mathrm{~A}, \mathrm{~T}_{\mathrm{A}}=25^{\circ} \mathrm{C}$ No Airflow, Natural Convection Cooling

## DEMO MANUAL DC2834A

## PARTS LIST

| ITEM | QTY | REFERENCE | PART DESCRIPTION | MANUFACTURER/PART NUMBER |
| :---: | :---: | :---: | :---: | :---: |
| Required Circuit Components |  |  |  |  |
| 1 | 1 | C1 | CAP., $0.1 \mu \mathrm{~F}, \mathrm{X7R}, 10 \mathrm{~V}, 10 \%$, 0603 | WURTH ELEKTRONIK, 885012206020 |
| 2 | 1 | C2 | CAP., $1 \mu \mathrm{~F}, \mathrm{X} 5 \mathrm{R}, 50 \mathrm{~V}, 10 \%$, 0603, AEC-Q200, NO SUBS. ALLOWED | MURATA, GRT188R61H105KE13D |
| 3 | 1 | C4 | CAP., 4.7 F , X5R, 50V, 10\%, 0805, AEC-Q200 | TDK, CGA4J3X5R1H475K125AB |
| 4 | 1 | C6 | CAP., $0.1 \mu \mathrm{~F}, \mathrm{X} 5 \mathrm{R}, 50 \mathrm{~V}, 10 \%$, 0603 | AVX, 06035D104KAT2A |
| 5 | 1 | C8 | CAP., 1000pF, COG, 50V, 5\%, 0603 | MURATA, GRM1885C1H102JA01D |
| 6 | 1 | C9 | CAP., 39pF, COG, 50V, 5\%, 0603 | AVX, 06035A390JAT2A |
| 7 | 1 | C10 | CAP., 4700pF, COG, 50V, 5\%, 0603 | MURATA, GRM1885C1H472JA01D |
| 8 | 2 | C24, C25 | CAP., 4.7 ${ }^{\text {F }}$, X5R, 16V, 10\%, 0805 | AVX, 0805YC475KAT2A |
| 9 | 4 | CIN1, CIN3, CIN10, CIN12 | CAP., 10ヶF, X7R, 63V, 10\%, 1210 | SAMSUNG, CL32B106KMVNNWE |
| 10 | 2 | CIN2, CIN7 | CAP., $56 \mu \mathrm{~F}$, ALUM. ELECT., $50 \mathrm{~V}, 20 \%$, 10x10.5mm RADIAL, HVH | SUN ELECTRONIC INDUSTRIES CORP, 50HVH56M |
| 11 | 2 | CIN5, CIN6 | CAP., 14F, X7R, 50V, 10\%, 0805 | AVX, 08055C105KAT2A |
| 12 | 2 | COUT2, COUT8 | CAP., 470 1 F, TANT. POSCAP, 6.3V, 20\%, 7343, $18 \mathrm{~m} \Omega$, TPE, NO SUBS. ALLOWED | PANASONIC, 6TPE470MI |
| 13 | 4 | COUT3-COUT6 | CAP., 100 ${ }^{\text {F , X }}$ KR, 6.3V, 20\%, 1206 | MURATA, GRM31CR60J107ME39L |
| 14 | 1 | D4 | DIODE, SCHOTTKY, 4OV, 120mA, SOD323-2, AEC-Q101 | INFINEON, BAS140W |
| 15 | 1 | L1 | IND., $0.68 \mu \mathrm{H}, \mathrm{WE}-\mathrm{CHSA}$ SMD HIGH CURRENT, $20 \%, 26 \mathrm{~A}, 1.7 \mathrm{~m} \Omega, 12.2 \mathrm{~mm} \times 12.2 \mathrm{~mm}$ | WURTH ELEKTRONIK, 7843320068 |
| 16 | 2 | Q1, Q2 | XSTR., MOSFET, N-CH, 40V, 98A, TDSON-8 | INFINEON, BSCO32N04LS |
| 17 | 2 | Q3, Q4 | XSTR., MOSFET, N-CH, 40V, 100A, TDSON-8 FL | INFINEON, BSC014N04LSI |
| 18 | 6 | R1, R7, R10, R12, R17, R27 | RES., AEC-Q200, $0 \Omega, 1 / 10 \mathrm{~W}, 0603$ | VISHAY, CRCW06030000Z0EA |
| 19 | 1 | R3 | RES., AEC-Q200, $2.2 \Omega, 5 \%, 1 / 10 \mathrm{~W}, 0603$ | VISHAY, CRCW06032R2OJNEA |
| 20 | 1 | R5 | RES., AEC-Q200, 1k, 1\%, 1/10W, 0603 | VISHAY, CRCW06031K00FKEA |
| 21 | 1 | R8 | RES., AEC-Q200, 1M 2 , 1\%, 1/10W, 0603 | VISHAY, CRCW06031M00FKEA |
| 22 | 1 | R9 | RES., AEC-Q200, 76.8k, 1\%, 1/10W, 0603 | VISHAY, CRCW060376K8FKEA |
| 23 | 1 | R11 | RES., AEC-Q200, 261k, 1\%, 1/10W, 0603 | VISHAY, CRCW0603261KFKEA |
| 24 | 1 | R13 | RES., 6.19k, 1/10W, 1\%, 0603 | YAGEO, RC0603FR-076K19L |
| 25 | 2 | R14, R24 | RES., AEC-Q200, 100k, 1\%, 1/10W, 0603 | VISHAY, CRCW0603100KFKEA |
| 26 | 1 | R15 | RES., AEC-Q200, 31.6k, 1\%, 1/10W, 0603 | VISHAY, CRCW060331K6FKEA |
| 27 | 1 | R16 | RES., 44.2 2 , 1\%, 1/10W, 0603, AEC-Q200 | VISHAY, CRCW060344R2FKEA |
| 28 | 1 | RS1 | RES., $0.002 \Omega$, $2 \%, 1.5 \mathrm{~W}, 1206$, LONG-SIDE TERM., METAL, SENSE, AEC-Q200, LOW EMF | SUSUMU, KRL3216E-M-R002-G-T5 |
| 29 | 1 | U1 | LOW I ${ }_{0}$ SYNCHRONOUS STEP-DOWN CONVERTER, 16-PIN SSOP | ANALOG DEVICES, INC., LTC7803EMSE\#PBF |

## DEMO MANUAL DC2834A

## PARTS LIST

| ITEM | QTY | REFERENCE | PART DESCRIPTION | MANUFACTURER/PART NUMBER |
| :---: | :---: | :---: | :---: | :---: |
| Additional Demo Board Circuit Components |  |  |  |  |
| 1 | 0 | C3, C5, C7, C11, C12, C14 | CAP., OPTION, 0603 |  |
| 2 | 0 | CIN4, CIN8, CIN9, CIN11 | CAP., OPTION, 1210 |  |
| 3 | 0 | CN1, CN2 | CAP., OPTION, 0402 |  |
| 4 | 0 | COUT1, COUT7 | CAP., OPTION, 7343 |  |
| 5 | 0 | D1 | DIODE, OPTION, SOD-123 |  |
| 6 | 0 | L1-L4 | IND., $0.68 \mu \mathrm{H}$, POWER, $20 \%, 38 \mathrm{~A}, 1.65 \mathrm{~m} \Omega$, $8.8 \mathrm{~mm} \times 8.3 \mathrm{~mm}$ | COILCRAFT, XAL8080-681MEB |
| 7 | 0 | R2, R6, R18, R21-R23, R25, R26 | RES., OPTION, 0603 |  |

## Hardware

| 1 | 8 | E1-E4, E6, E8-E10 | TEST POINT, TURRET, 0.094", MTG. HOLE | MILL-MAX, 2501-2-00-80-00-00-07-0 |
| :---: | :---: | :--- | :--- | :--- |
| 2 | 2 | J1, J2 | CONN., BANANA JACK, FEMALE, THT, NON- <br> INSULATED, SWAGE | KEYSTONE, 575-4 |
| 3 | 2 | J3, J4 | STUD, FASTENER, \#10-32 | RING, LUG, CRIMP,\#10, NON-INSULATED, <br> SOLDERLESS TERMINALS |
| 4 | 2 | J3, J4 KEYSTONE, 8205 |  |  |
| 5 | 4 | J3, J4 | NUT, HEX, \#10-32, BRASS | PENCOM, NU1132 |
| 6 | 2 | JP1, JP3 | CONN., HDR, MALE, $2 \times 3,2 m m$, VERT, STR, THT | WURTH ELEKTRONIK, 62000621121 |
| 7 | 1 | JP2 | CONN., HDR, MALE, $1 \times 3,2 m m, ~ S T R, ~ T H T, ~ N O ~$ <br> SUBS. ALLOWED | WURTH ELEKTRONIK, 62000311121 |
| 8 | 2 | J3, J4 | WASHER, \#10, LOCK, EXT, TIN FINISH | PENCOM, WA4526 |
| 9 | 4 | MH1-MH4 | STANDOFF, NYLON, SNAP-ON, 0.625" | KEYSTONE, 8834 |
| 10 | 3 | XJP1-XJP3 | CONN., SHUNT, FEMALE, 2 POS, 2mm | WURTH ELEKTRONIK, 60800213421 |

## SCHEMATIC DIAGRAM



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